

Page 2, please delete the paragraph at line 27 and insert therefor:

A2
--Figure 4 shows a timing diagram useful to understand the Figure 3 schematic.--

Page 3, please delete the paragraph between lines 2 through 9 and insert therefor:

A3
--Transmitter output impedance and transmission line impedance matching and termination technique is a concern if the signals rising/falling timing is comparable with the flight time through the transmission line. In some electronic interfaces, it is very important that the impedance of a transmitter match the characteristic line impedance and that a receiver connected thereto be capable of operating like an open circuit. Still other interfaces employ additional line termination techniques in order to prevent reflection at the receiving end. In the later case, the additional terminated device or devices can be installed at the receiving end to minimize possible sources of reflection.--

Page 5, please delete the paragraph between lines 18 through 20 and insert therefor:

A4
--Figure 2B shows a slightly more detailed schematic as compared with Figure 2A, in which the tracking system 10 is a dv/dt analyzer operating a charge pump 11 to compensate for charging or discharging of parasitic capacitor C_p .--

Page 5, please delete the paragraph between lines 21 through 27 and insert therefor:

A5
--With reference to Figure 2C, the input signal is shown applied to the I/O pad P through the transmission line represented by the impedance Z_L . The tracking system employs